

FIG. 1

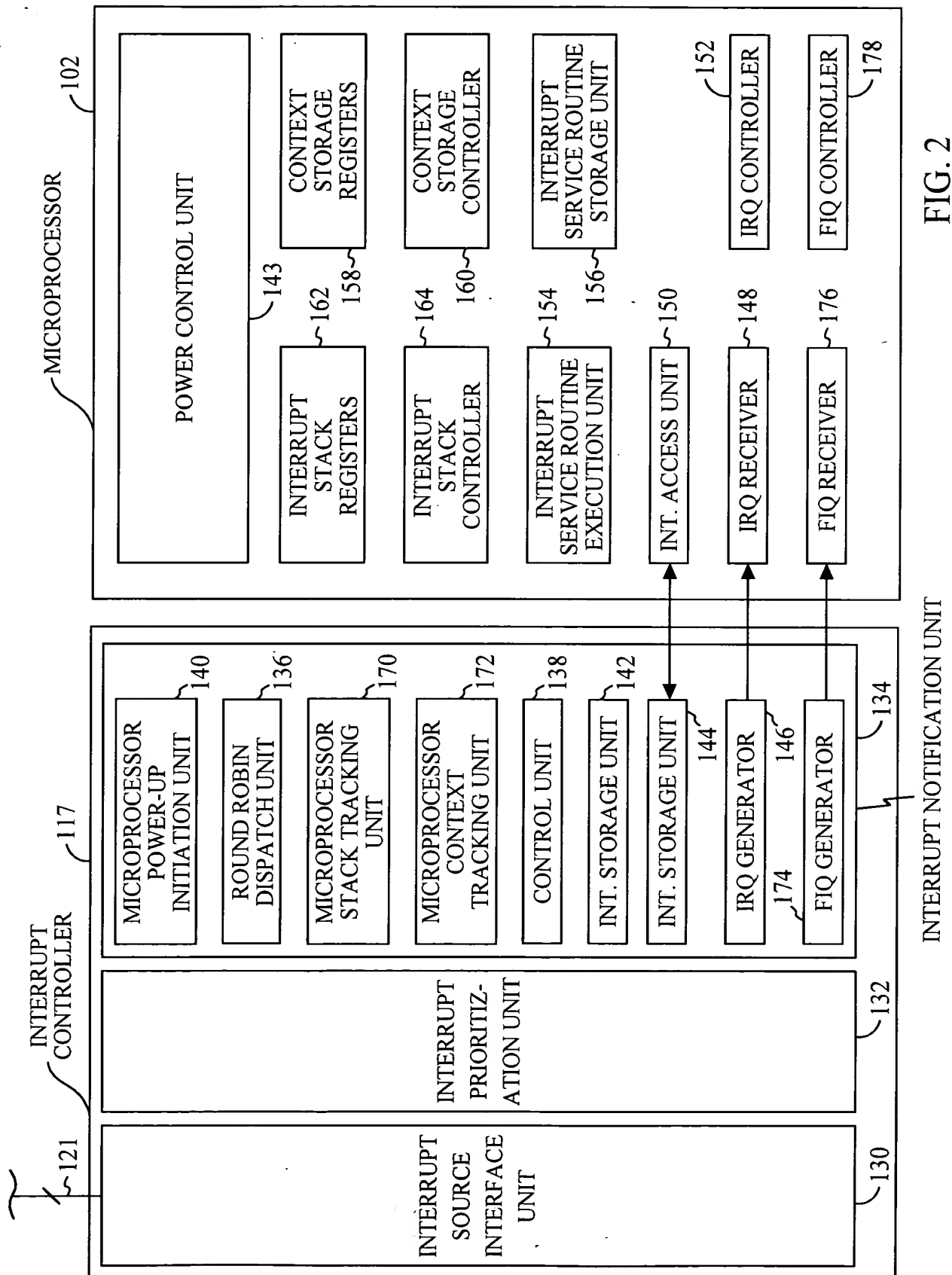


FIG. 2

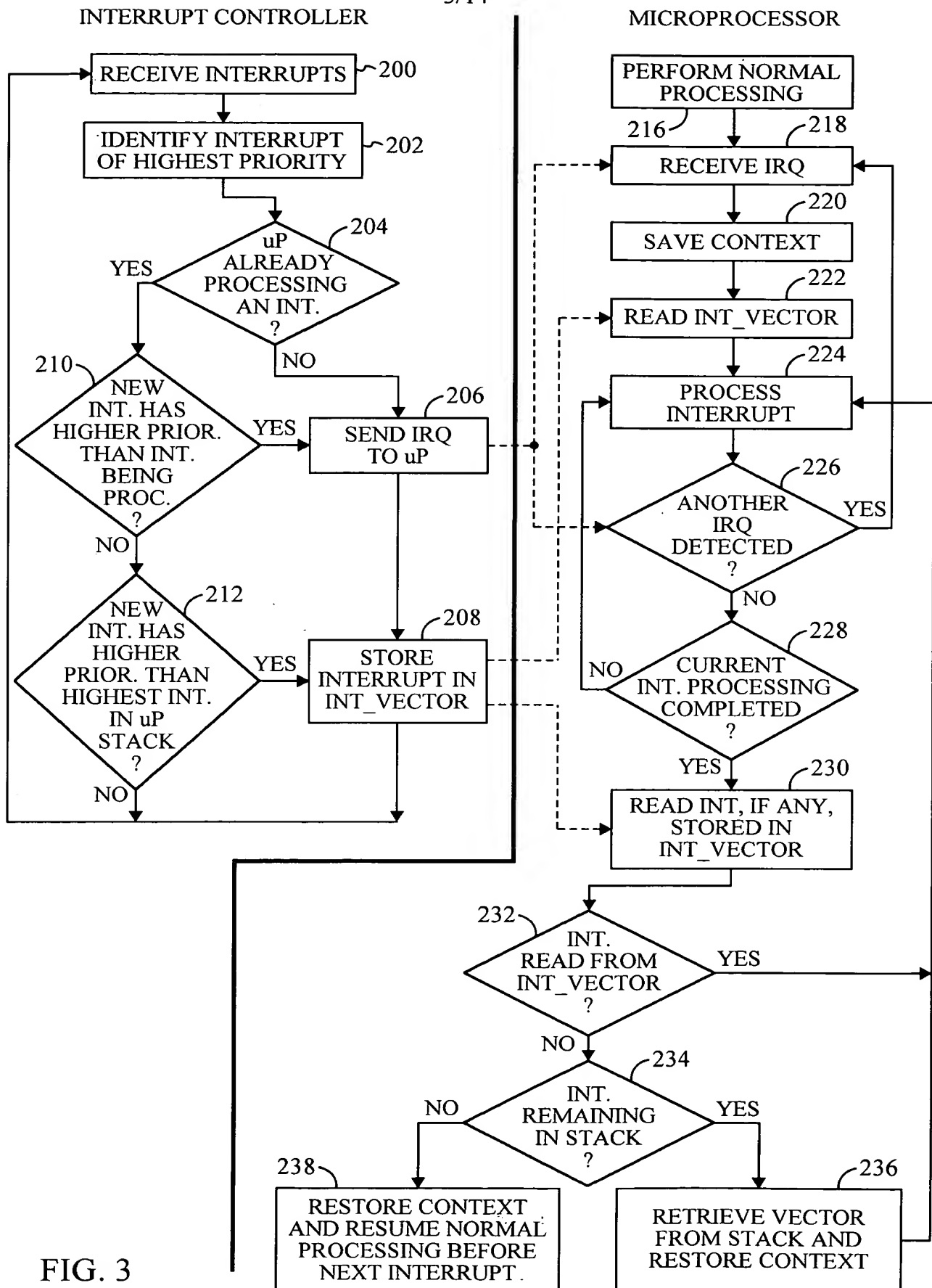


FIG. 3

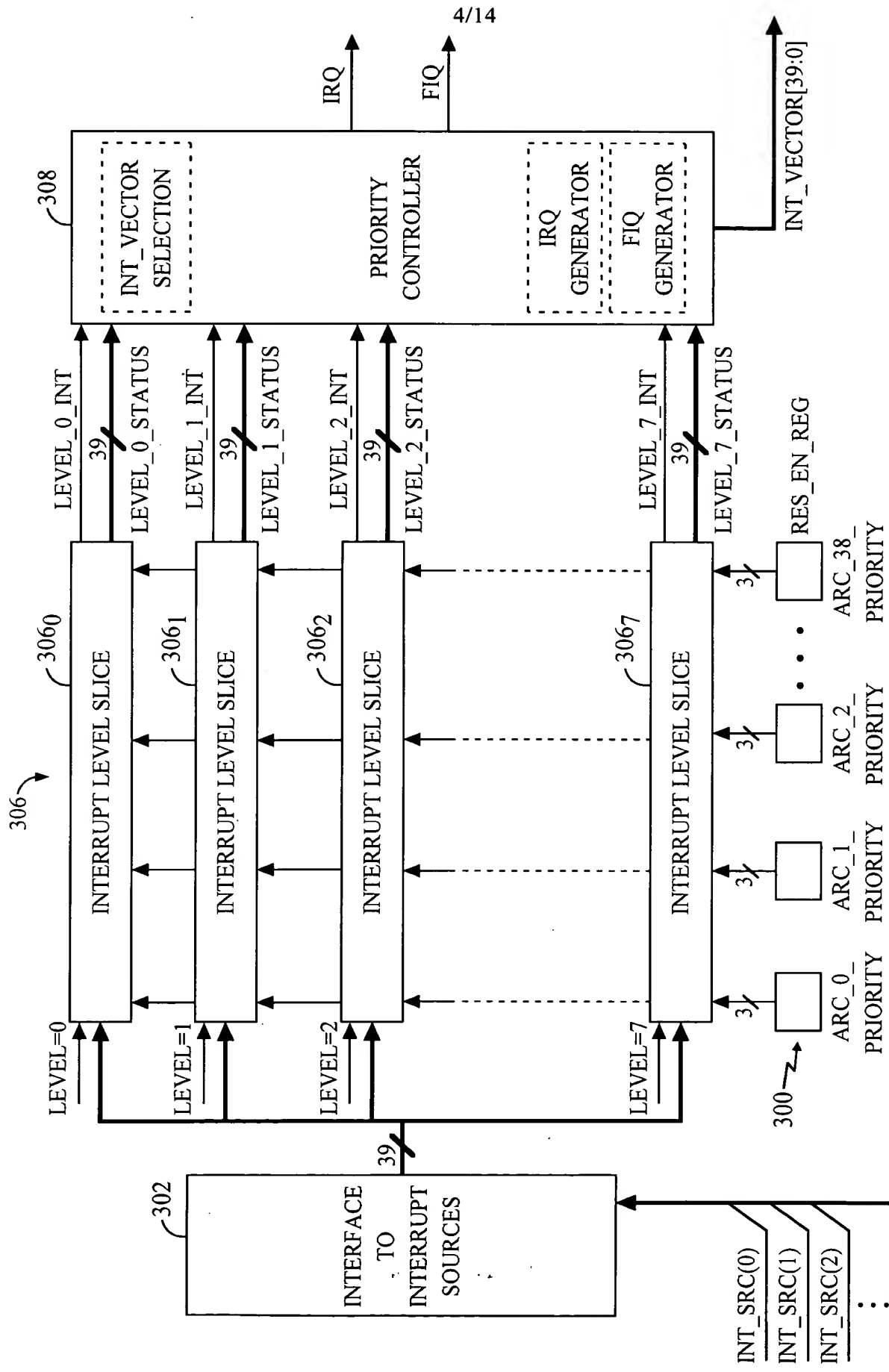


FIG. 4

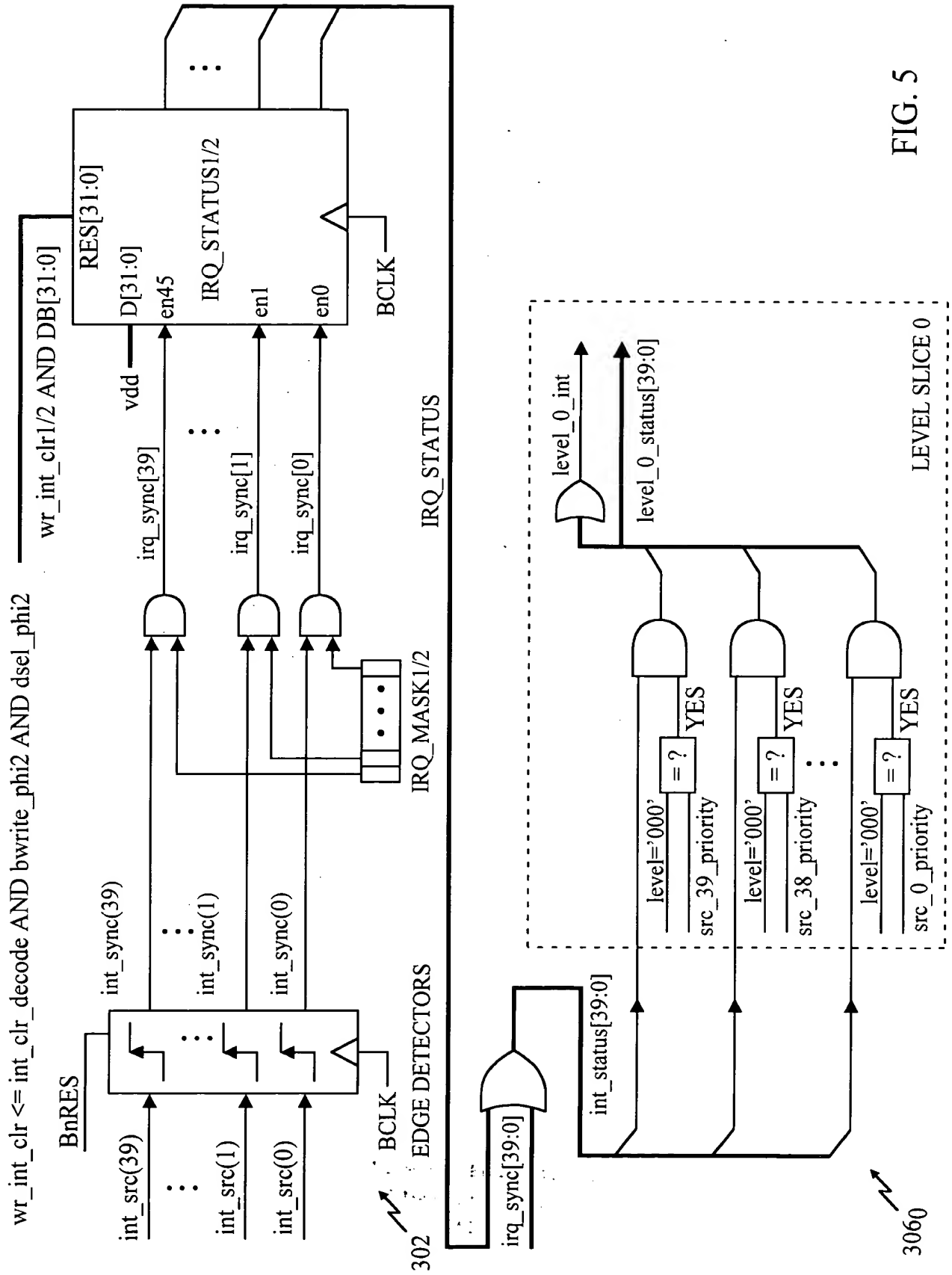
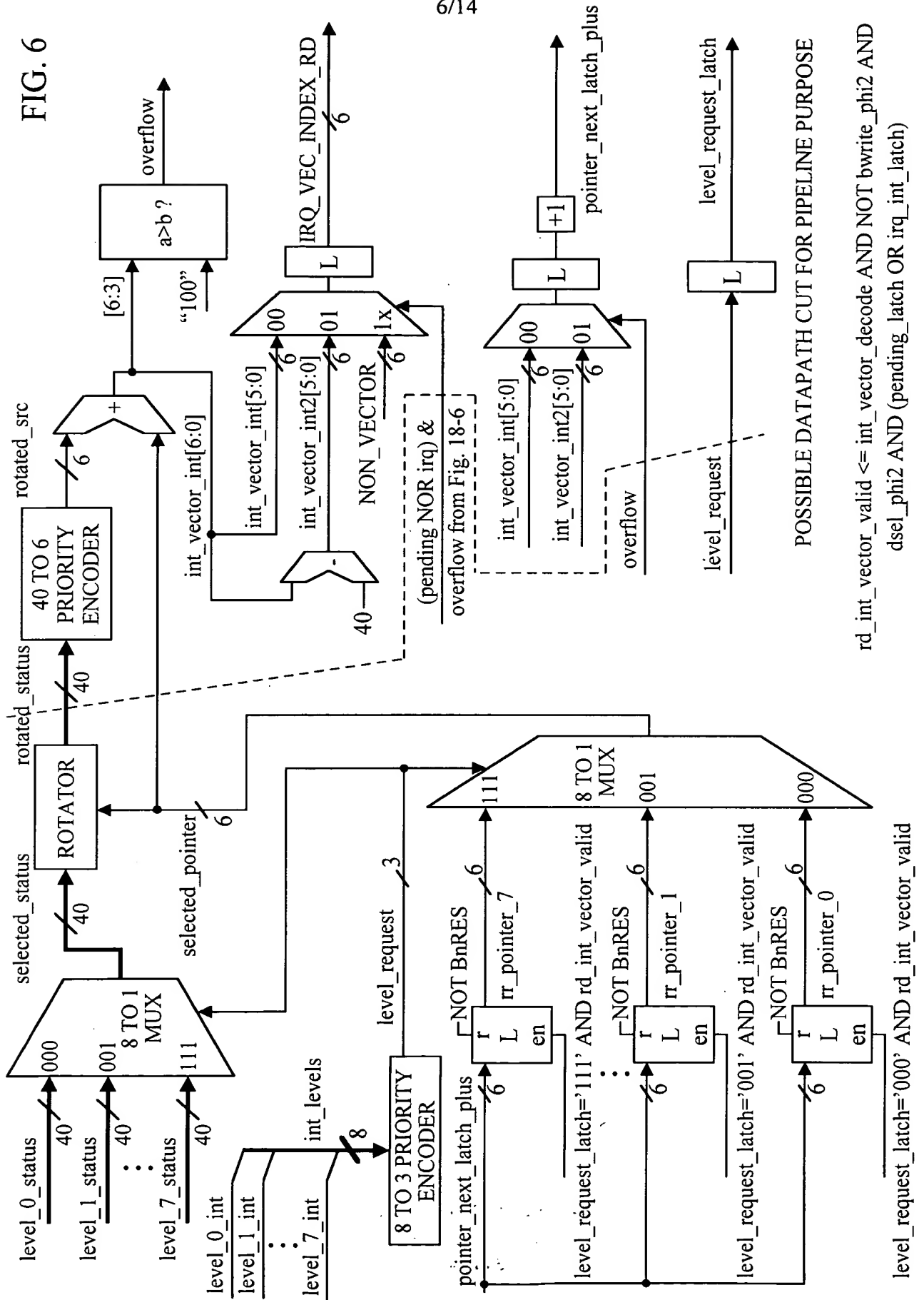


FIG. 5

FIG. 6



POSSIBLE DATAPATH CUT FOR PIPELINE PURPOSE

```
rd_int_vector_valid <= int_vector_decode AND NOT bwrite_phi2 AND
    dsel_phi2 AND (pending_latch OR irq_int_latch)
```

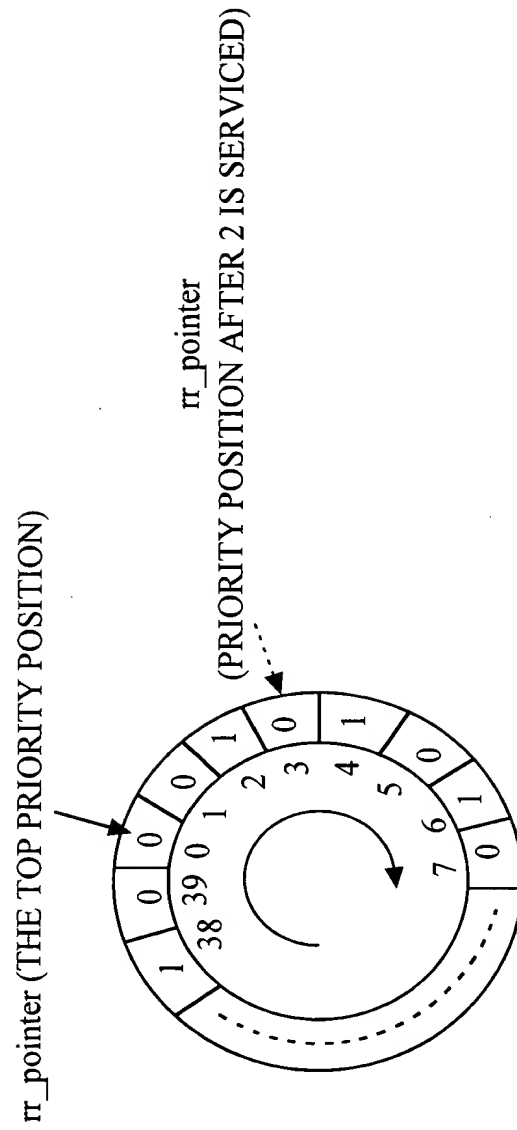


FIG. 7

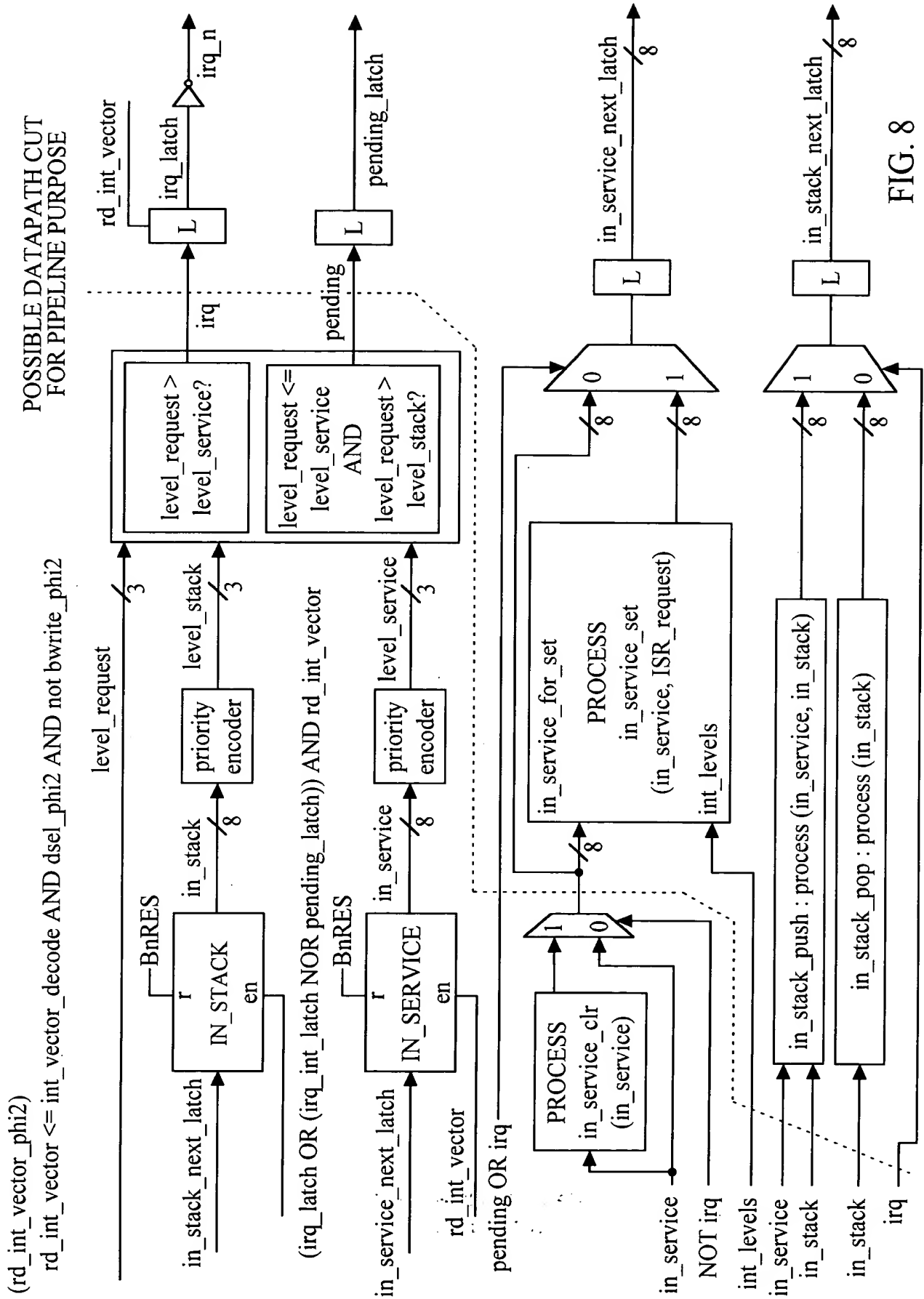


FIG. 8

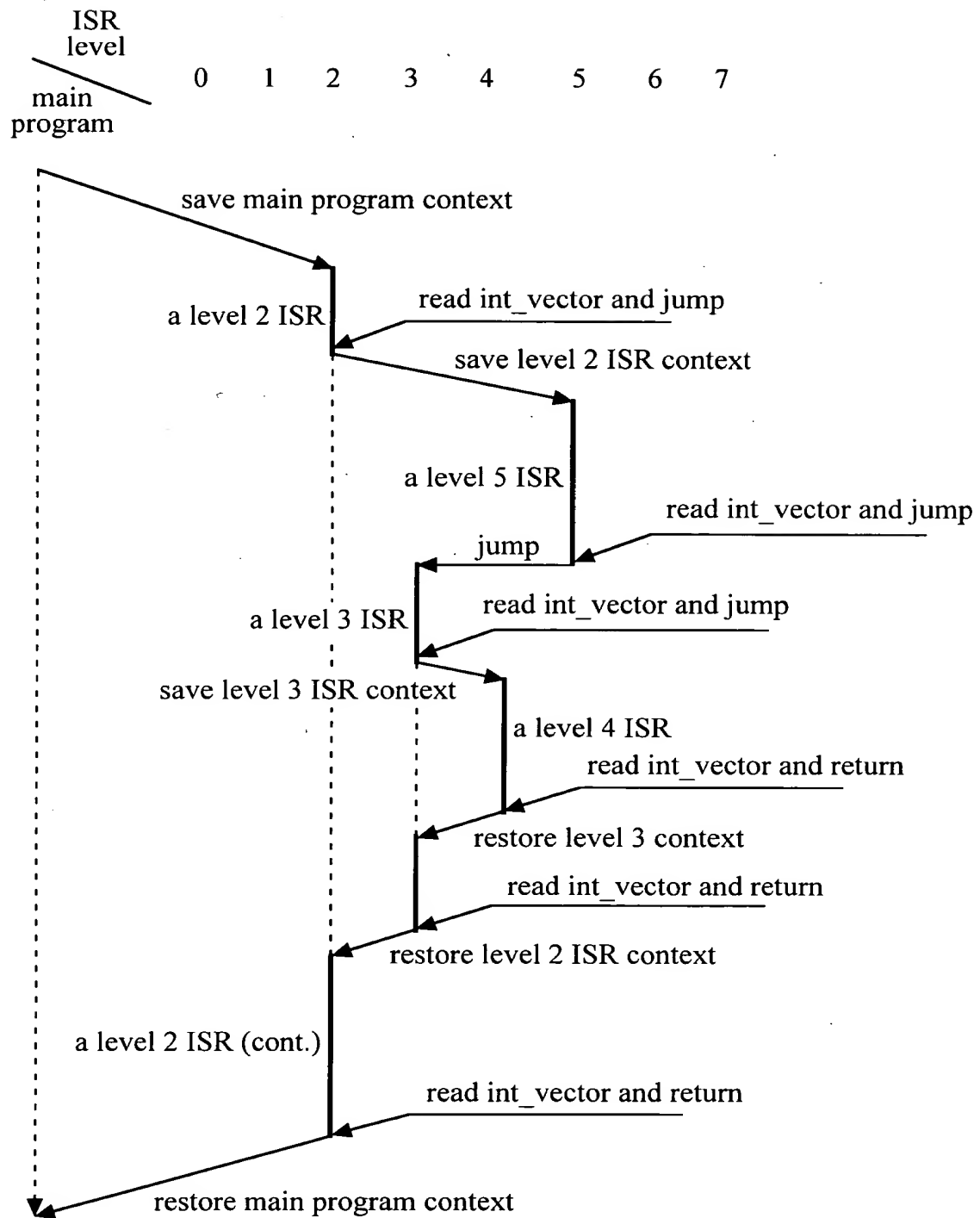


FIG. 9

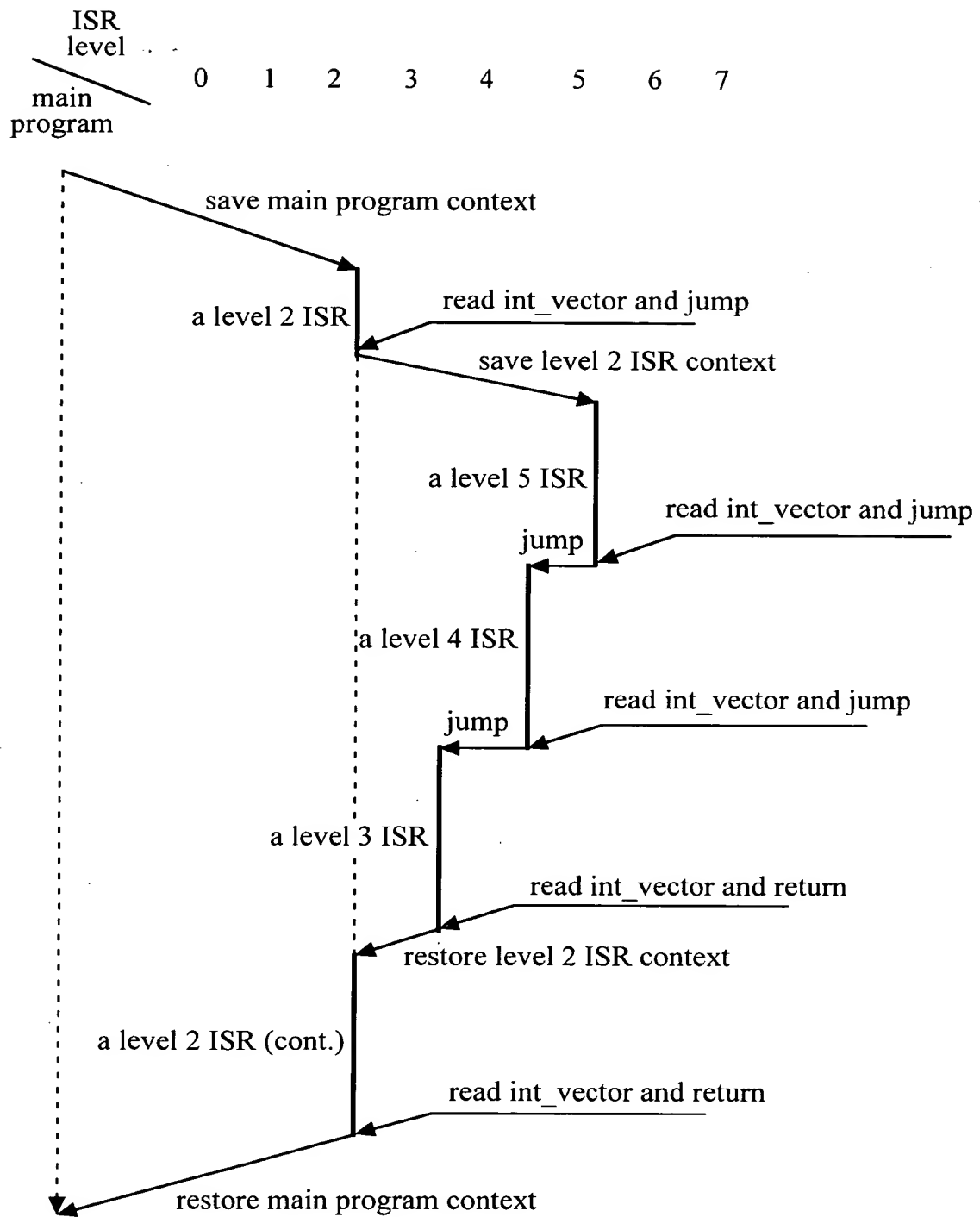


FIG. 10

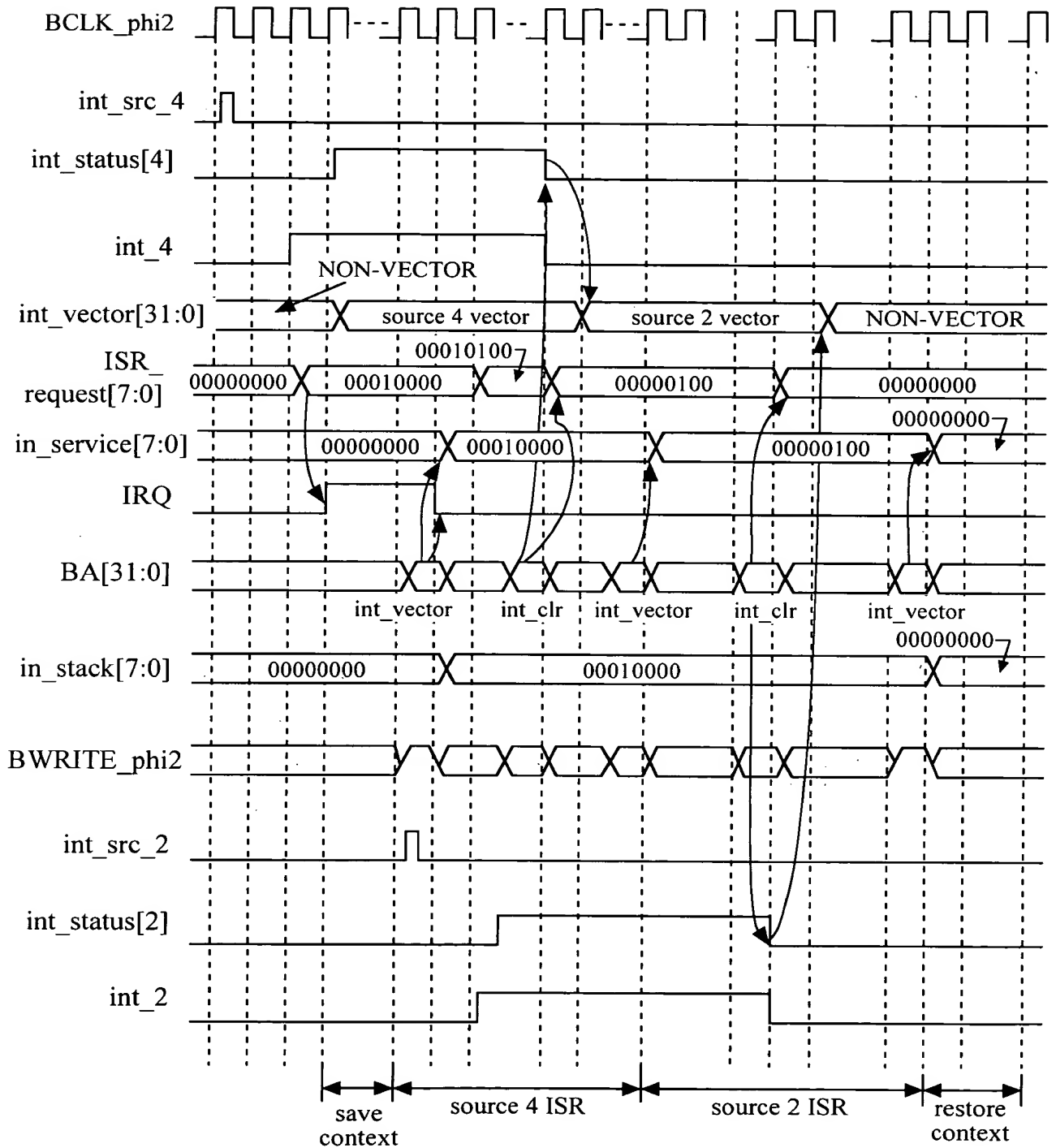


FIG. 11

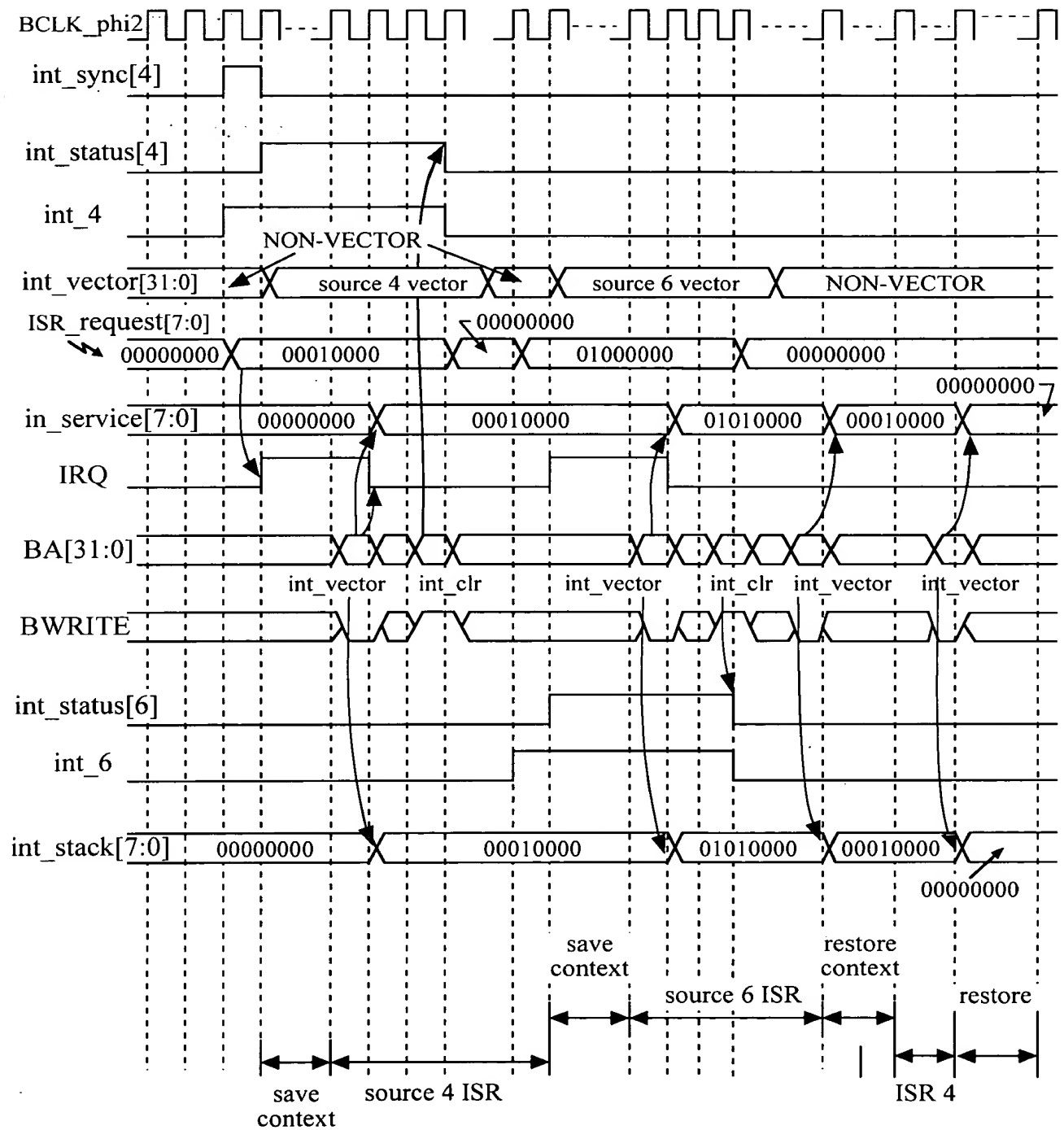


FIG. 12

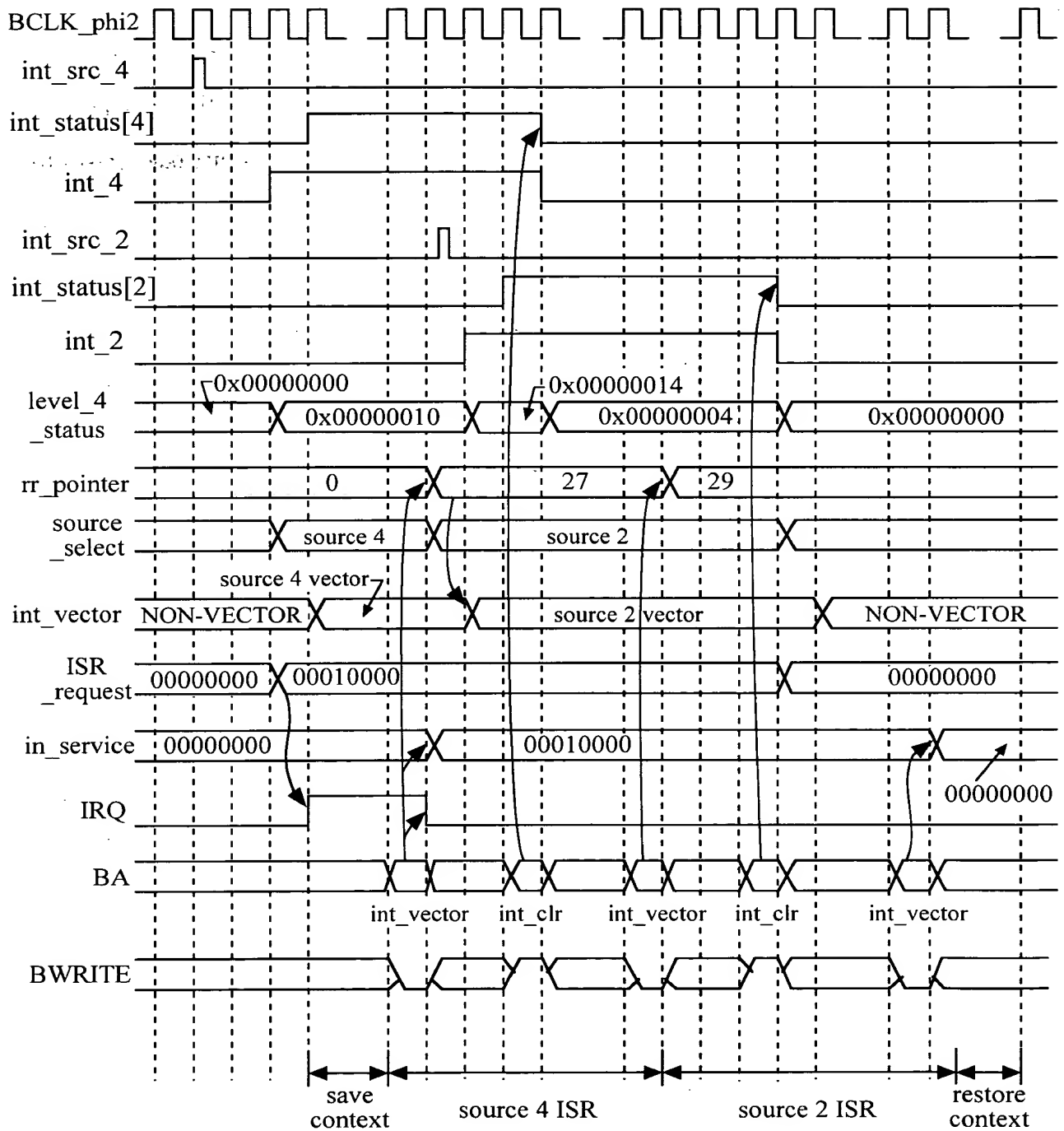


FIG. 13

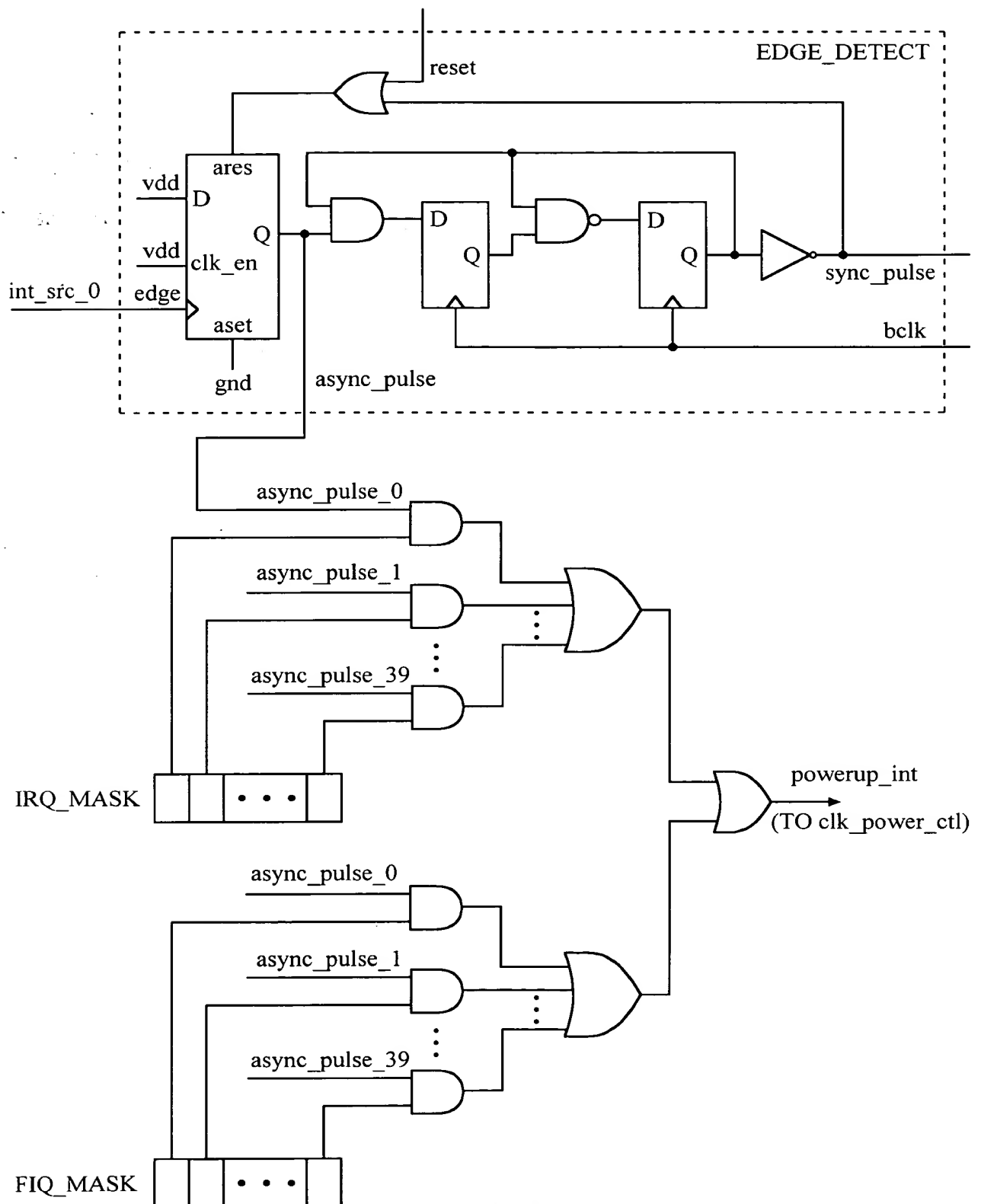


FIG. 14